

G.V.M.'s G.G.P.R. College of Commerce and Economics
Farmagudi, Ponda-Goa.

B.C.A. (Semester I) Intra Semester Assessment (I.S.A.) Test-I October 2020

COMPUTER ORGANISATION AND ARCHITECTURES

Duration:- 45 minutes

Marks:- 20

ANSWER THE FOLLOWING :-

1. What are the various improvements in Chip Organization and Architecture ? (2 mks)
2. Explain the Fetch Cycle and the Execute Cycle. (3 mks)
3. Write a short note on ENIAC. (3 mks)
4. Explain the basic Computer Operations. Also, draw the block diagram of Computer. (4 mks)
5. Explain Multiple Interrupts. Also, elaborate on Sequential Multiple Interrupts. (4 mks)
6. Explain the various elements of Bus design. (4 mks)
